



NPN SILICON POWER DARLINGTON TRANSISTOR

С

CSD 1025

TO-220 **Plastic Package**

UNIT

V

V

V

А А

А А

W

°C

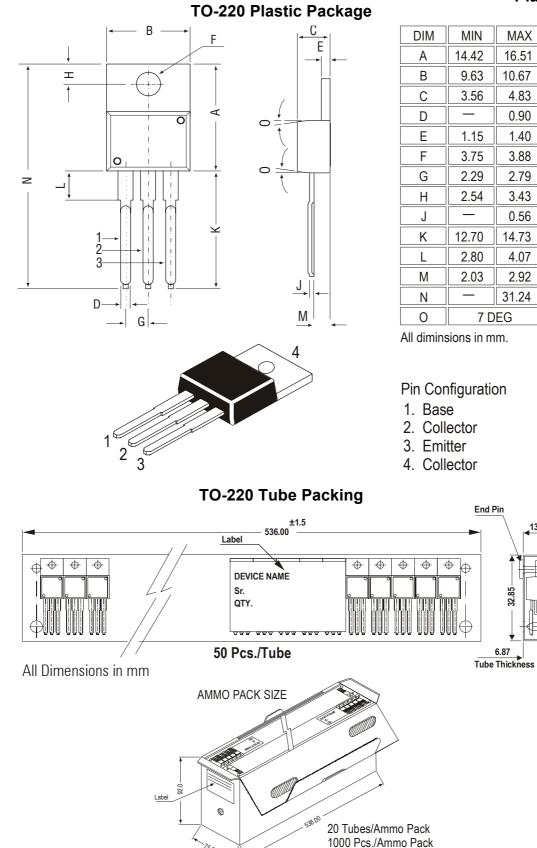
°C/W

BC									
E									
ABSOLUTE MAXIMUM RATINGS (Ta=25	5⁰C unless	specifi	ed otherwise)						
DESCRIPTION	SY	MBOL	VALUE						
Collector -Base Voltage	١	/ _{CBO}	200						
Collector -Emitter Voltage	١	/ _{CEO}	200						
Emitter Base Voltage		V _{EBO}	7.0						
Collector Current DC		I _C	8.0						
F	Peak	I _{CP}	12.0						
Base Current DC		I _B	0.5						
F	Peak	I _{BP}	1.0						
Total Power Dissipation @Tc=25°C		PD	50						
Operating And Storage Junction	Т	i, T _{stq}	-55 to +150						
Temperature Range], ota							
THERMAL RESISTANCE									
Junction to Case	F	th(j-c)	2.5						
		/							
ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)									
DESCRIPTION	SY	MBOL	TEST CONDITION	MIN	TYP	MAX			
Collector Cut off Current		I	(-200)/(-0)			0.1			

ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)									
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT			
Collector Cut off Current		V _{CB} =200V, I _E =0			0.1	mA			
	I _{CEO}	V _{CE} =200V, I _B =0			0.1	mA			
Emitter Cut off Current	I _{EBO}	V _{EB} =7V, I _C =0			5	mA			
Collector Emitter Saturation Voltage	V _{CE(sat)}	I _C =5A, I _B =10mA			1.5	V			
Base Emitter Saturation Voltage	V _{BE(sat)}	I _C =5A, I _B =10mA			2.0	V			
DC Current Gain	h _{FE}	I_{C} =5A, V_{CE} =3V	1.5		30	K			
DYNAMIC CHARACTERISTICS									
Transition Frequency	f _T	I_{C} =0.8A, V_{CE} =10V		50		MHz			
SWITCHING TIME									
Turn on Time	t _{on}				2	μs			
Storage Time	t _{stg}	$I_{B1} = I_{B2} = 10 \text{ mA}, I_{C} = 5 \text{ A},$			8	μs			
Fall time	t _f	$R_L = 5\Omega, V_{BB2} = 4V$		5	μs				

13.74

TO-220 Plastic Package



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
T0-220	200 pcs/polybag 50 pcs/tube	396 gm/200 pcs 135 gm/50 pcs	3" x 7.5" x 7.5" 3.5" x 3.7" x 21.5"	1K 1K	17" x 15" x 13.5" 19" x 19" x 19"	16K 10K	36 kgs 28 kgs

TO-220 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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